

ALTERNATING VOIDED AREAS OF ANTI-PADS

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Abstract

A printed circuit board comprises a first conductive plane and a second conductive plane substantially parallel to the first conductive plane. The printed circuit board comprises a via signal barrel transecting the first and second conductive planes and a first anti-pad positioned between the first conductive plane and the via signal barrel. The first anti-pad has a first voided area. The
10 printed circuit board comprises a second anti-pad positioned between the second conductive plane and the via signal barrel. The second anti-pad has a second voided area. The first voided area does not completely overlap the second voided area.